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Fig. 1 PRIOR ART

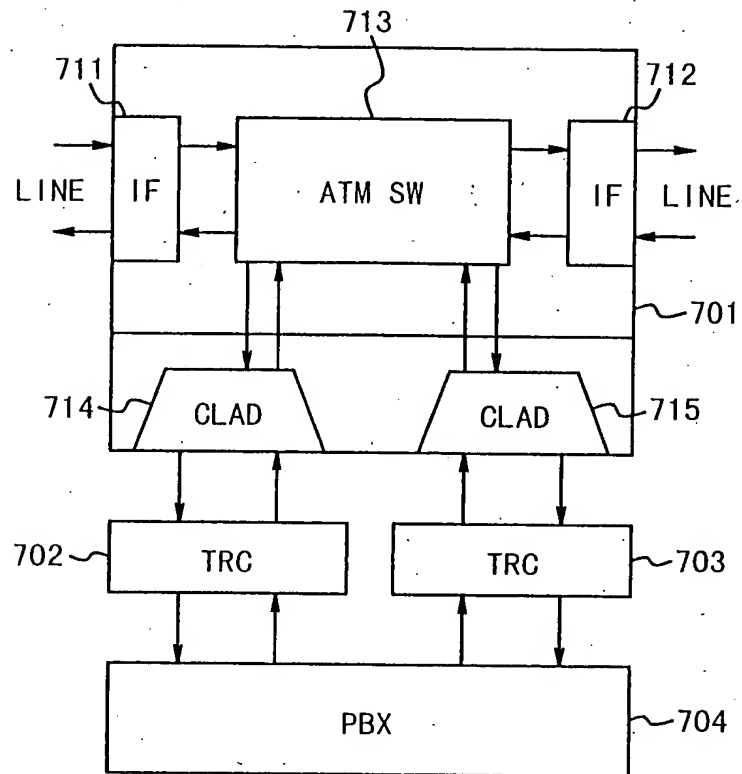


Fig. 2

- 101, 201, 301 -
 first cell disassembling section
 102, 202, 302 -
 second cell disassembling section
 103, 203, 303 - decoder
 104, 204, 304 - multiplexer
 105, 205, 305 - signal generator
 106, 206, 306 - address indicator
 107, 207, 307 - controller
 108, 208, 308 - detector
 109, 209, 309 -
 second cell assembling section
 110, 210, 310 - selector
 111, 211, 311 - coder
 112, 212, 312 -
 first cell assembling section

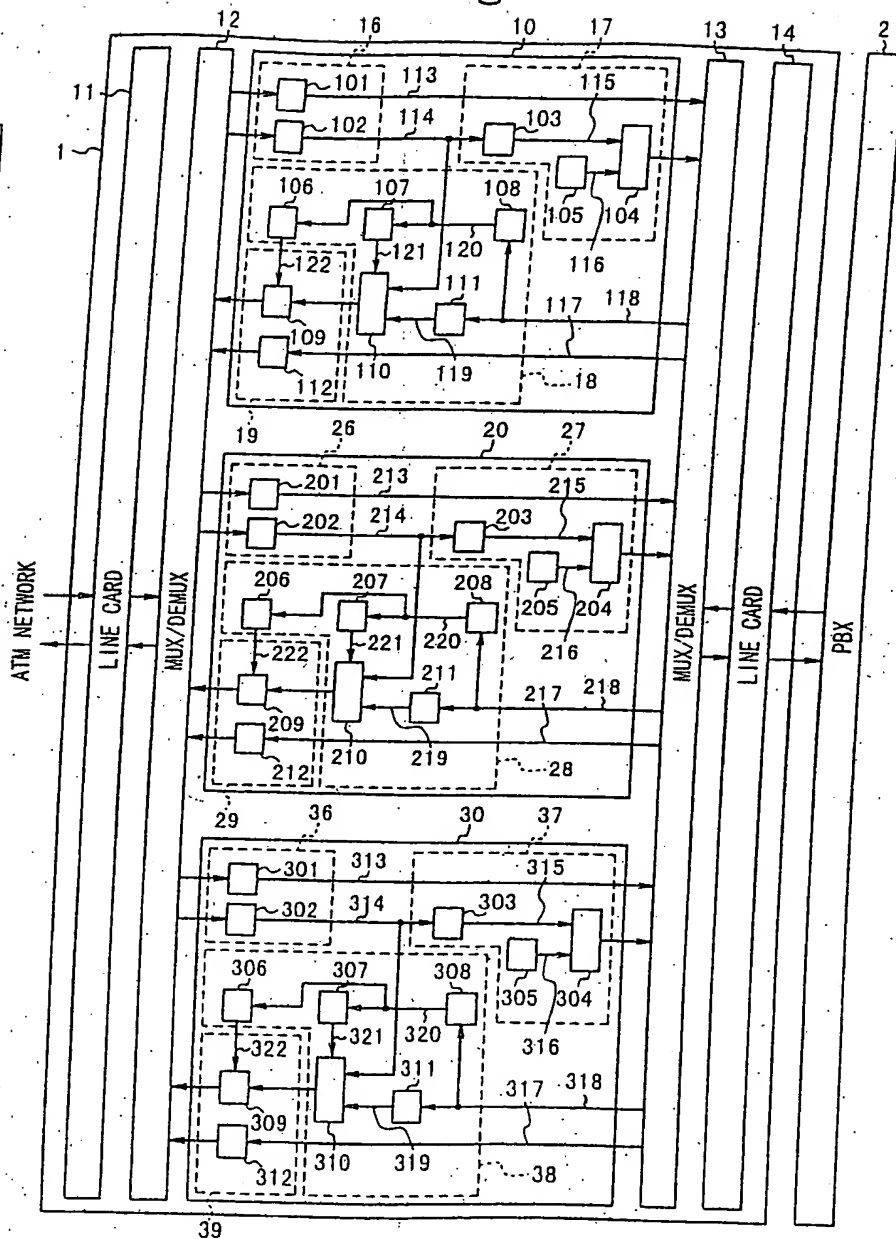


Fig. 3

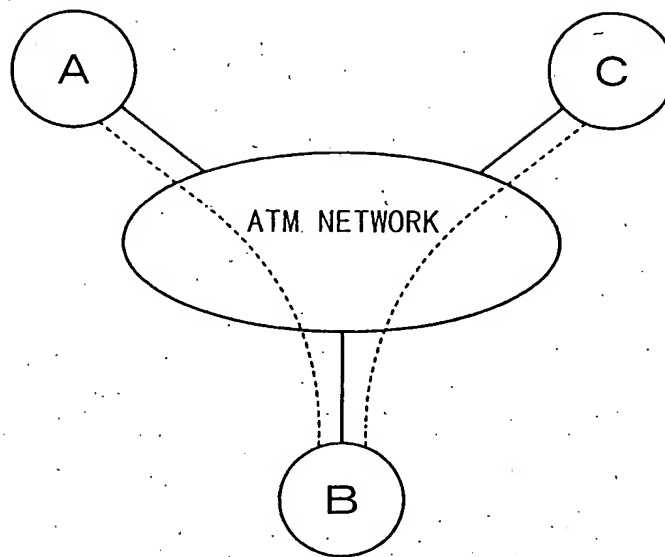


Fig. 4

ADDRESS	bs1	bv1	bs2	bv2	bs3	bv3
DESTINATION	CLD 101	CLD 102	CLD 201	CLD 202	CLD 301	CLD 302

Fig. 5

DETECTION SIGNAL	NOTHING	SYNCHRONOUS SIGNAL 116	SYNCHRONOUS SIGNAL 216	SYNCHRONOUS SIGNAL 316
ADDRESS SIGNAL	av1	av1	av2	cv1

Fig. 6

DETECTION SIGNAL	NOTHING	SYNCHRONOUS SIGNAL 116	SYNCHRONOUS SIGNAL 216	SYNCHRONOUS SIGNAL 316
ADDRESS SIGNAL	av2	av1	av2	cv1

Fig. 7

DETECTION SIGNAL	NOTHING	SYNCHRONOUS SIGNAL 116	SYNCHRONOUS SIGNAL 216	SYNCHRONOUS SIGNAL 316
ADDRESS SIGNAL	bv1	av1	av2	cv1